Blakely, Sokoloff, Taylor & Zafman LLP (310) 207-3800 Title: UTILIZING AN ADVANCED LOAD ADDRESS TABLE FOR MEMORY DISAMBIGUATION IN AN OUT OF ORDER PROCESSOR (310) 207-3800

1st Named Inventor: Gary N. Hammond Express Mail No.: EV339916357US

Docket No.: 42P18219

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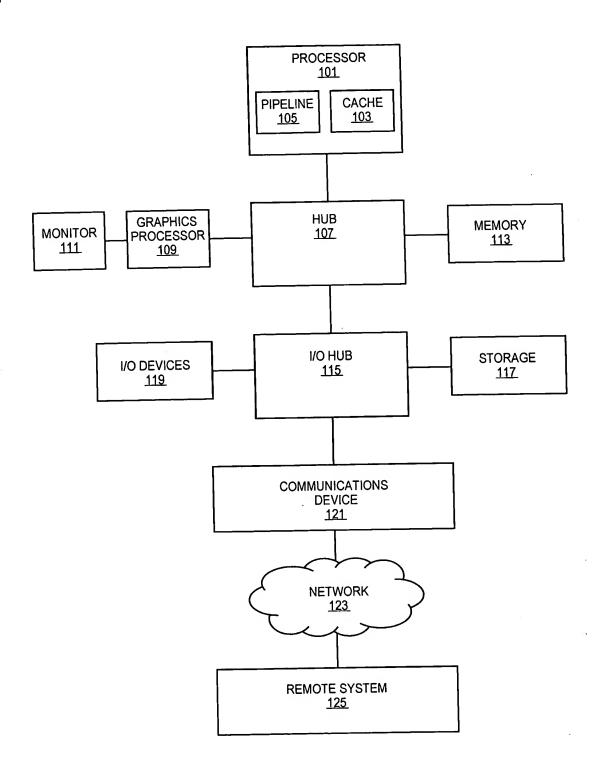
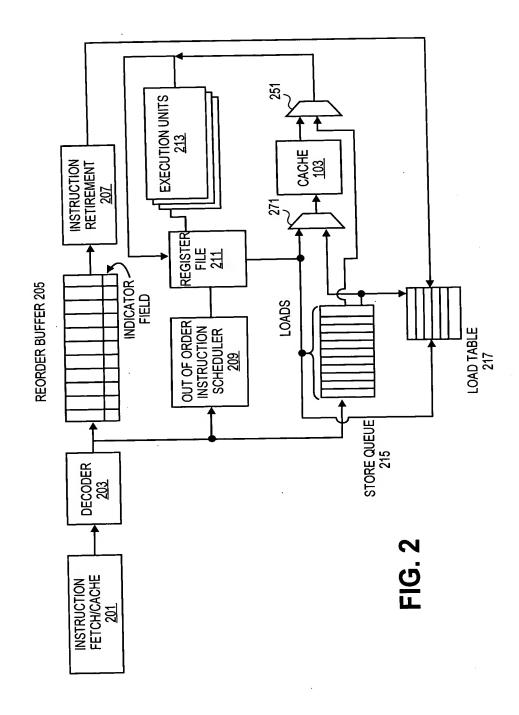


FIG. 1

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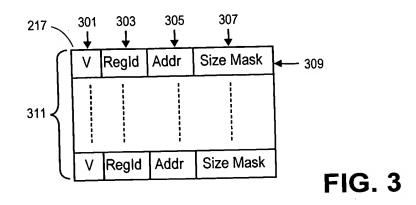
PROCESSOR

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$$401 \left\{ \begin{array}{c} St8 \ [r2] = r2 \\ 403 \left\{ \begin{array}{c} Ld2 \ r3 = [r4] \end{array} \right\} 405 \\ St8 \ [r2] = r2 \\ Ld2 \ c \ r3 = [r4] \end{array} \right\} 401$$

$$Ld2 \ c \ r3 = [r4] \left\{ \begin{array}{c} 401 \\ 409 \\ 423 \end{array} \right\}$$

FIG. 4

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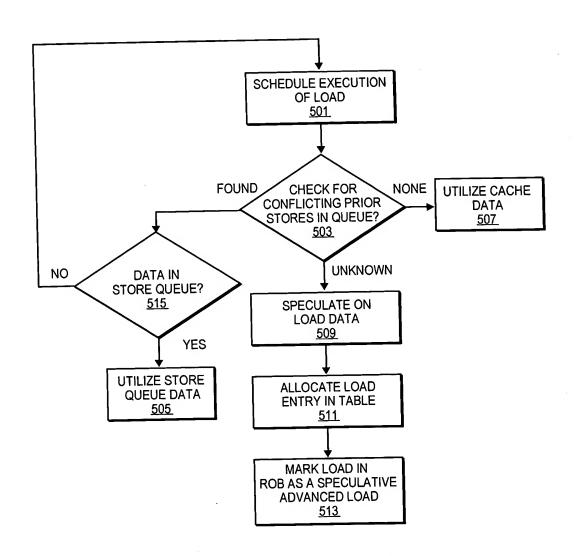


FIG. 5

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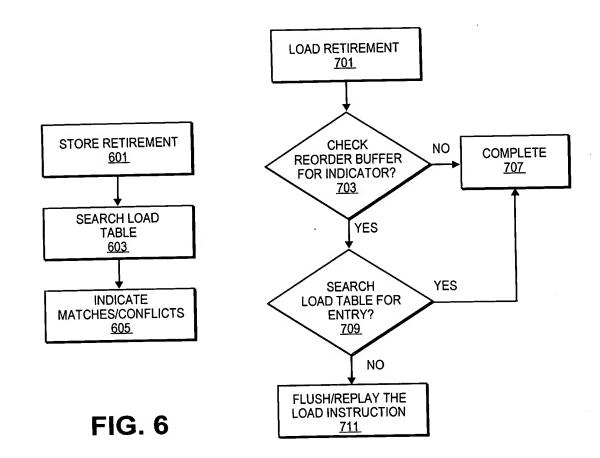


FIG. 7